

FIG. 6



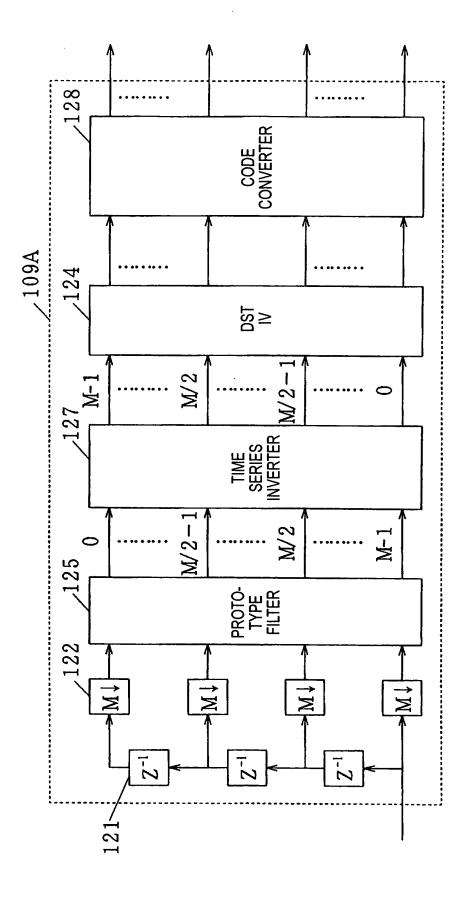
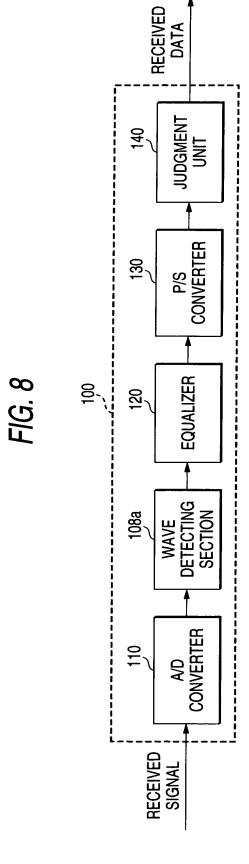
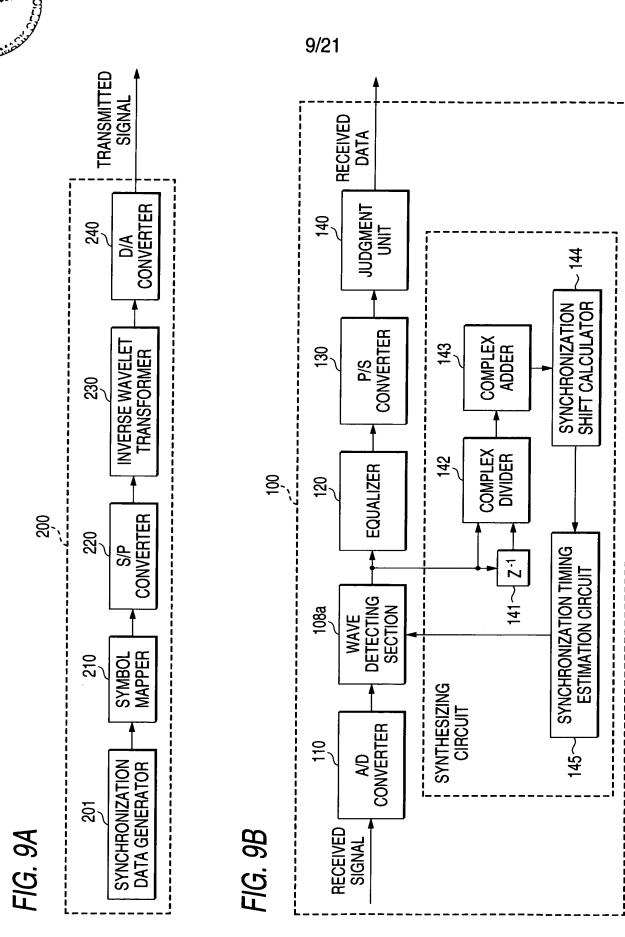


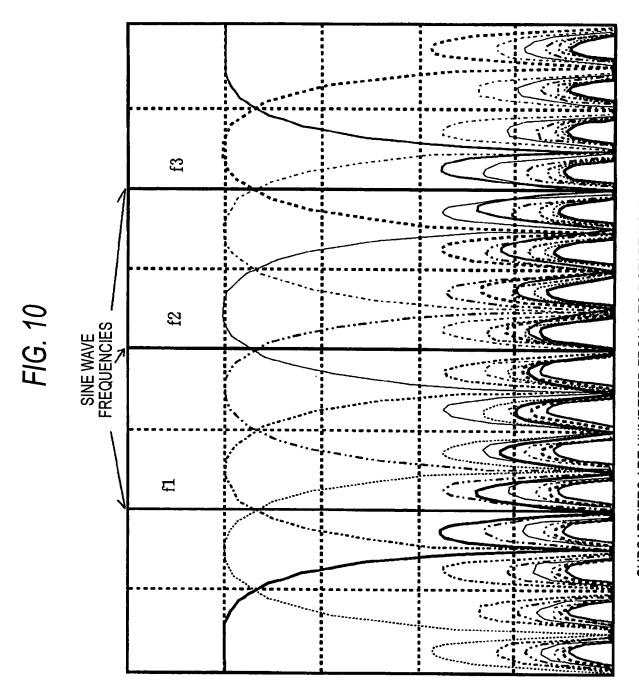
FIG. 1





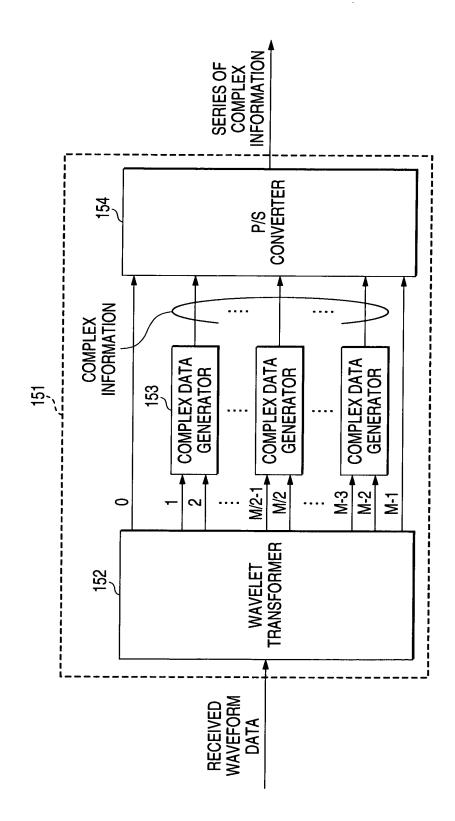






SUBCARRIERS ARE NUMBERED FROM 0 TO 7 STARTING ON LEFT SIDE







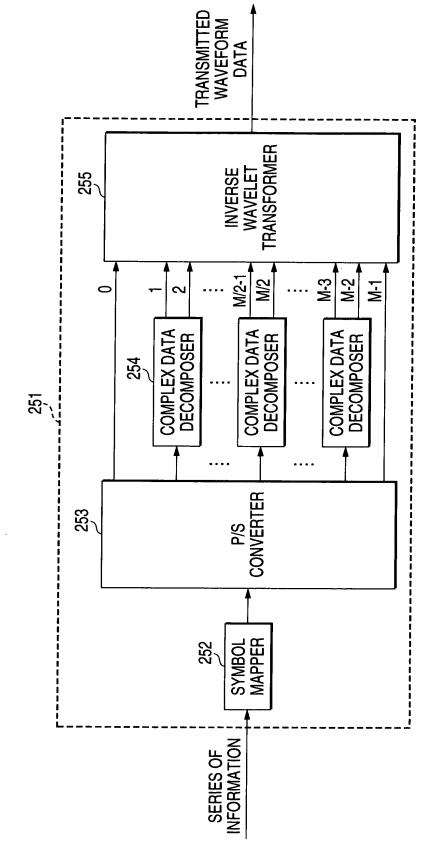


FIG. 12

SYNCHRONIZATION SHIFT CALCULATOR

SYNCHRONIZATION TIMING

145~

ESTIMATION CIRCUIT



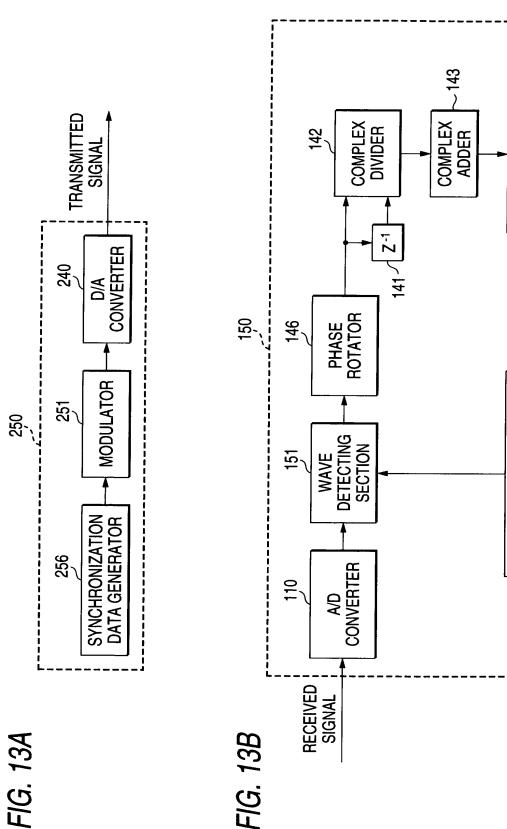
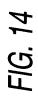
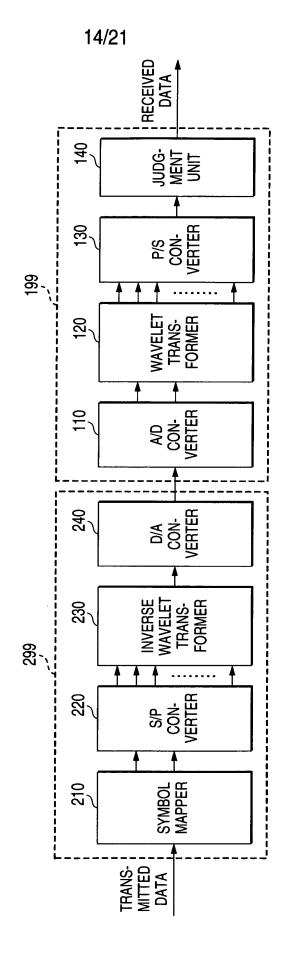


FIG. 13A











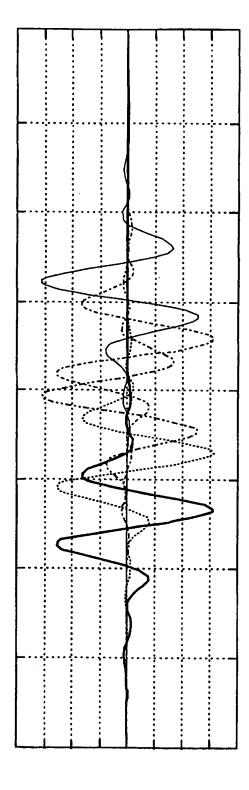
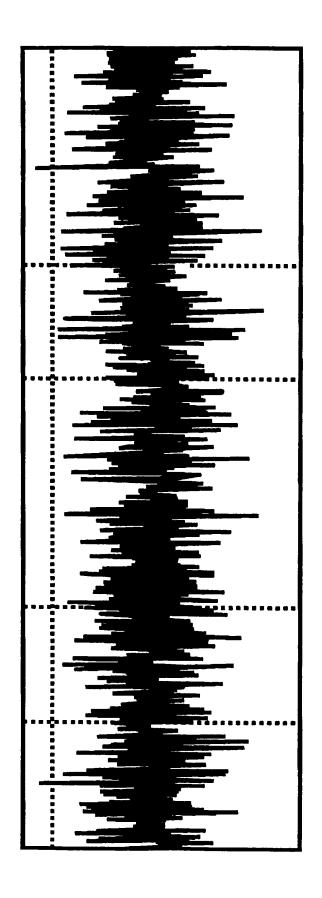




FIG. 16







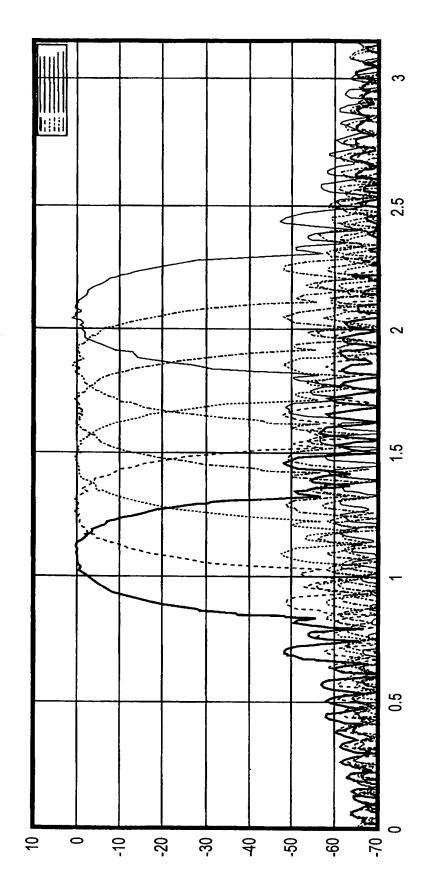
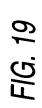


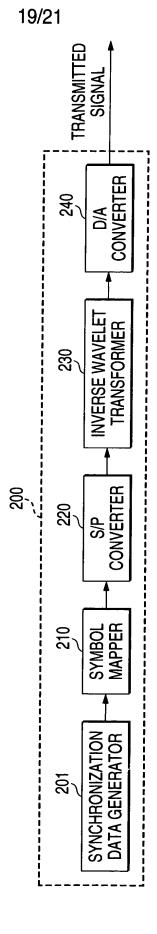


FIG. 18

SYNCHRONI- ZATION	EQUALI- ZATION	INFORMATION SYMBOL
SYMBOL	SYMBOL	











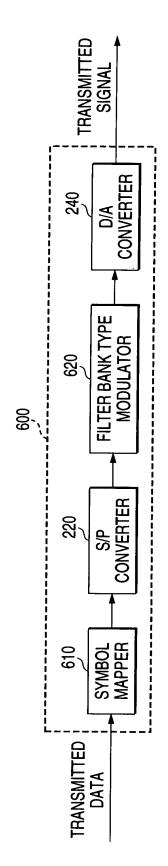


FIG. 21

